

AUG 26 2005



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From: Jon Y. Ikegami
650.833.2104 Client-Matter Number: 357060-991100
(2505332)

Re: U.S. Patent Application No. 10/789,066
Filed: February 27, 2004
Entitled: METHOD AND ARCHITECTURE FOR INTEGRATED CIRCUIT DESIGN AND
MANUFACTURE
Inventor: MALEKKHOSRAVI et al.

Pages: - 12 - (including this form) Originals: will not follow

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Message:

Please see the attached Response to First Office Action for the above-referenced application.

Applicants do not believe there are fees associated with this reply, however, the Commissioner is authorized to charge any fees which may be required to Deposit Account No. 07-1896 (Docket No. 357060-991100).

Thank you.

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PTO/SB/21 (09-04)

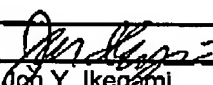
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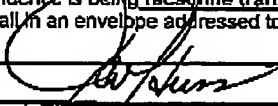
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/789,066	
	Filing Date	February 27, 2004	
	First Named Inventor	MALEKKHOSRAVI et al.	
	Art Unit	2825	
	Examiner Name	Vuthe Siek	
Total Number of Pages in This Submission	11	Attorney Docket Number	357060-991100 (2505332)

ENCLOSURES (Check all that apply)		
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Remarks The Commissioner is authorized to charge any additional fees which may be required, including petition fees and extension of time fees, to Deposit Account No. 07-1896 (Docket No. 357060-991100). A duplicate copy of this paper is enclosed.		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	DLA Piper Rudnick Gray Cary US LLP	
Signature		
Printed name	Jon Y. Ikegami	
Date	August 26, 2005	Reg. No. 51,1115

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Typed or printed name	Jan Huss	Date August 26, 2005

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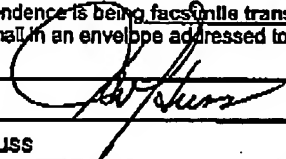
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/789,066	
	Filing Date	February 27, 2004	
	First Named Inventor	MALEKKHOSRAVI et al.	
	Art Unit	2825	
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Firm Name	DLA Piper Rudnick Gray Cary US LLP		
Signature			
Printed name	Jon Y. Ikegami		
Date	August 26, 2005	Reg. No.	51,1115

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Typed or printed name	Jan Huss	Date	August 26, 2005

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August 26, 2005 By: Jan Huss

Appl. No. : 10/789,066
Applicant : Malekkhosravi et al.
Filed : February 27, 2004
TC/A.U. : 2825
Examiner : Vuthe Siek

Confirmation No. 9171

Docket No. : 357060-991100
Customer : 26379
No.

Title: METHOD AND ARCHITECTURE FOR INTEGRATED CIRCUIT DESIGN AND
MANUFACTURE

RESPONSE TO FIRST OFFICE ACTION

M/S: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request the Examiner to consider the following Response in reply to the
Office Action, mailed on May 27, 2005, for the above-referenced application.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2
of this paper.

Remarks/Arguments begin on page 7 of this paper.

PAXI0422879.1
357060-991100